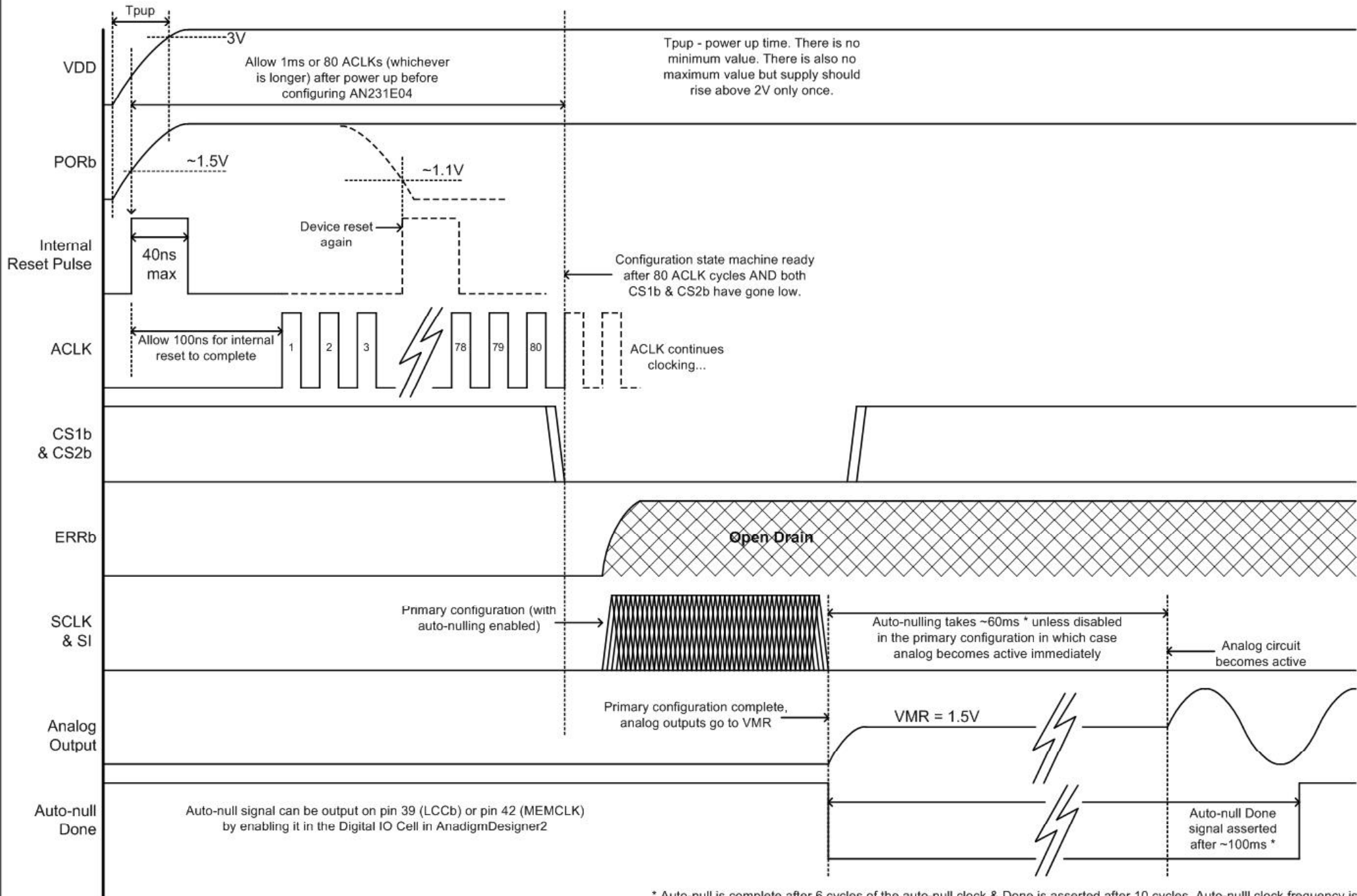


# POWER ON SEQUENCE OF AN231E04



\* Auto-null is complete after 6 cycles of the auto-null clock & Done is asserted after 10 cycles. Auto-null clock frequency is:  $F_{an} = F_{ack} / (1000 \times Div)$  where Div is set by AnadigmDesigner2 to make  $F_{an}$  as near to 100Hz as possible.